oplication No.	Applicant(s)
1/707 403	WHITE, THOMAS H. ET AL.
	Art Unit
ndrew O. Tran	2824
idlew Q. Traff	2024
R REMAINS) CLOSED in other appropriate common of the commo	ith the correspondence address in this application. If not included unication will be mailed in due course. THIS subject to withdrawal from issue at the initiative
en received. en received in Application nents have been received his communication to file T of this application.  d. Note the attached EX eason(s) why the oath o	
	w ( PTO-948) attached
mendment / Comment o	r in the Office action of
	he drawings in the front (not the back) of FR 1.121(d).
	ERIAL must be submitted. Note the OLOGICAL MATERIAL.
6. ☐ Interview S Paper No. 7. ☐ Examiner's	Informal Patent Application (PTO-152)  Summary (PTO-413),  /Mail Date  Amendment/Comment  Statement of Reasons for Allowance
	R REMAINS) CLOSED in other appropriate comm ITS. This application is a d MPEP 1308.  It as U.S.C. § 119(a)-(d)  It are received.  It are received in Application in the communication to file. It of this application.  It as the attached EX eason(s) why the oath one is submitted.  It is Patent Drawing Review mendment / Comment on the communication to the attached in the communication.  It is Patent Drawing Review mendment / Comment on the communication of the communication in the communication.  It is patent Drawing Review mendment / Comment on the communication of the communication of the communication.  It is patent Drawing Review mendment / Comment on the communication of the communication of the communication.  It is patent Drawing Review mendment / Comment on the communication of the communicat

## **REASONS FOR ALLOWANCE**

The following is an examiner's statement of reasons for allowance:

Apparatus claims 1 and 10, and corresponding method claim 11 call for a programmable logic device (PLD) comprising a control circuit coupled to a memory for controlling said memory to selectively operate at a first rate under a first power state, and at a second rate under a second power state. Additionally apparatus claim 9 requires a control circuit for controlling a programmable logic device (PLD) comprising a first and second sense amplifier, coupled to a memory, respectively operate at a first and second rate according to a first and second power state, based on a power selection signal.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## **Drawings**

The informal drawings submitted are acceptable for examination purposes. New formal drawings are required in response to this Office action. Objections to the drawings will not be held in abeyance.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Andrew Q. Tran whose telephone number is (571) 272-1885. The examiner can normally be reached on Mon - Fri 8:30 AM - 5:30 PM.

Application/Control Number: 10/797,493 Page 3

Art Unit: 2824

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard T. Elms can be reached on (571) 272-1869. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Andrew Q. Tran Primary Examiner Art Unit 2824

at September 28, 2004